Notice of Allowability	Application No.	Applicant(s)
	10/764,093	CAO ET AL.
	Examiner	Art Unit
	Mujtaba K. Chaudry	2133
The MAILING DATE of this communication apperalled all claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in thi or other appropriate communic IGHTS. This application is subj	s application. If not included ation will be mailed in due course. THIS
1. This communication is responsive to <u>9/6/2005</u> .		
2. The allowed claim(s) is/are 21-38.		
3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some* c) None of the:		
1. Certified copies of the priority documents have been received.		
2. Certified copies of the priority documents have been received in Application No		
3. Copies of the certified copies of the priority documents have been received in this national stage application from the		
International Bureau (PCT Rule 17.2(a)).		
* Certified copies not received: Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		
4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.		
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.		
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached		
1) hereto or 2) to Paper No./Mail Date		
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date	s Amendment / Comment or in t	he Office action of
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t		
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.		
Attachment(s) 1. ☐ Notice of References Cited (PTO-892)	5. □ Notice of Inform	nal Patent Application (PTO-152)
2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)	6. ☐ Interview Sumr	• • • • • • • • • • • • • • • • • • • •
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0	Paper No./Mai	I Date
Paper No./Mail Date	•	
4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. ☐ Examiner's Sta 9. ☐ Other	tement of Reasons for Allowance
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Application/Control Number: 10/764,093

Art Unit: 2133

REASONS FOR ALLOWANCE

Claims 21-38 are allowed. The following is an Examiner's statement of reasons for allowance:

Independent claim 1 of the present application teaches a circuit for testing a transceiver, comprising: a test pattern generator generating a first test pattern and a second test pattern; a multiplexer having a first input, a second input, and an output, the first input receiving the first test pattern and the second input receiving the second test pattern; a demultiplexer having an input, a first output, and a second output, the input of the demultiplexer being coupled the output of the multiplexer; and a test result evaluation circuit generating a first signature based on a first data stream generated by the first output of the demultiplexer and a second signature based on a second data stream generated by the second output of the demultiplexer, the test result evaluation circuit comparing each generated signature to a stored signature for determining whether an error has occurred during the transmission of an associated data stream. The prior art of record, namely Yamada, basically teaches a communication circuit fault detector that tests a communication circuit by using a sequence generator to generate a test signal, a memory circuit to temporarily store the test signal, and a multiplexer to multiplex the test signal and an input data signal into the communication circuit. A timing generator drives the communication circuit at a rate higher than the normal communication rate, enabling the data signal and test signal to be sent through the communication circuit simultaneously. A de-multiplexer that de-multiplexes the output signal from the communication circuit into an output data signal and a returned test signal. A comparator that is used to compare the returned test signal with the test signal stored in the memory circuit and generates a result signal indicating whether they match. However, Yamada

Application/Control Number: 10/764,093

Art Unit: 2133

does not teach several aspects of the present application. For example, Yamada does not teach

nor fairly suggests the test pattern generator to generate a first and second test patterns and feed

them to the multiplexer. Yamada also does not teach nor does fairly suggest each of the

particular test pattern to be associated with a signature. Furthermore, it is the Examiner's

conclusion that the non-obvious difference between the prior art of record and the present

invention is that the present invention improves on the prior art by using a plurality of test

patterns with associated signatures to test a transceiver.

Independent claims 30 includes similar limitations recited in independent claim 21 and

therefore are allowed as well.

Claims 22-29 and 31-38 depend from independent claims 21 and 30 and inherently

include limitations therein and therefore are allowed as well.

Any inquiries concerning this communication should be directed to the examiner,

Mujtaba Chaudry who may be reached at 571-272-3817. The examiner may normally be reached

Mon – Thur 6:30 am to 4:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, please contact the

examiner's supervisor, Albert DeCady at 571-272-3819.

Mujtaba Chaudry

Art Unit 2133

September 27, 2005

SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100

Page 3